

XA-9689A
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Norikatsu TAKAURA et al.

Appln. No.: 10/616,355

Group Art Unit: 2813

Filed: July 10, 2003

Examiner: J. Dolan

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
PRODUCTION METHOD THEREOF

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

It is respectfully requested that the time now set for response be extended, retroactively, by three months. The required petition fee for a large entity is submitted herewith. If any further extension of time is required to avoid abandonment of this application, such extension is hereby requested. The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper to Deposit Account No. 50-1165.

05/18/2005 HALI11 00000105 10616355

01 FC:1253

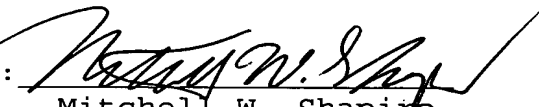
1020.00 DP

Respectfully submitted,

MWS:sjk

Miles & Stockbridge P.C.
1751 Pinnacle Drive
Suite 500
McLean, Virginia 22102-3833
(703) 903-9000

By:


Mitchell W. Shapiro
Reg. No. 31,568

May 17, 2005